FIG. 1

Single-Hop Packet (excluding inversion bits)

FIG. 2

Single-Flit Packet (excluding 11 inversion bits)

47 bits

30 bits

PAYLOAD — 204

CRC (Payload & Seq.#) — 206

0   46   47   77

202

FIG. 3

Payload - 47 bits — 302

Sequence # 12bits — 306

47bits

12 bit Seq #

CRC — 304
30 bit CRC(P)

CRC — 308
30 bit CRC(s)

47 bits

30 bit CRC(t) — 310

77 bits

Encoder / Inversion bits — 312

88 bit Packet (one flit)

To Serial Link
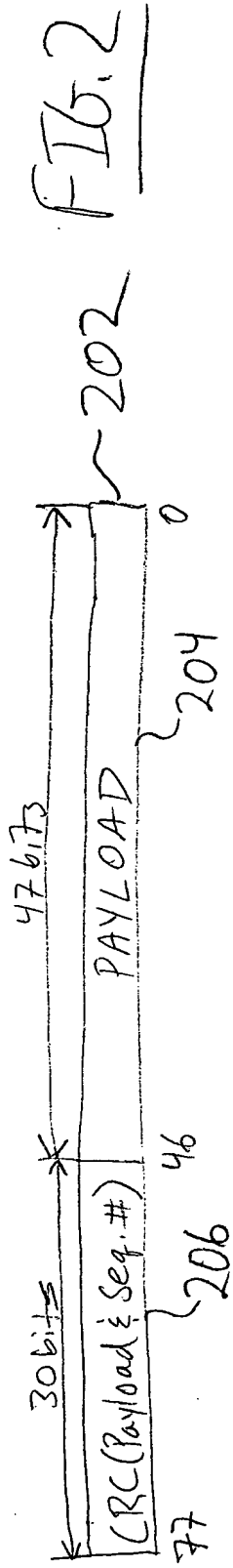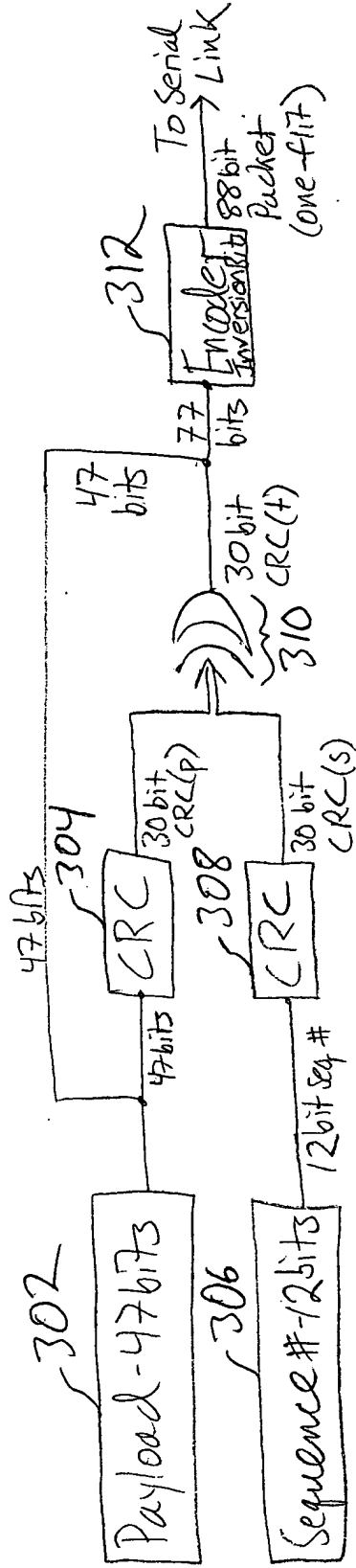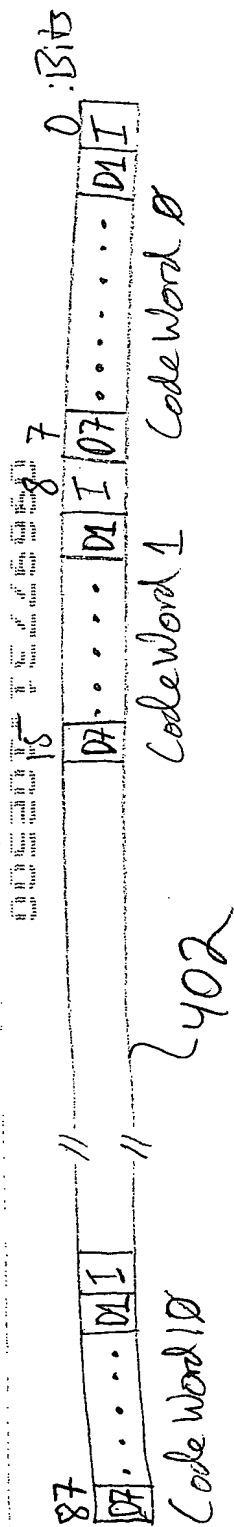
FIG. 4A

FIG.4B

Receiver Node

Receive encoded 88-bit packet — 404

Strip out invert bits (77 bits left) — 406

Input into CRC generator — 408

CRC generator computes 32-bit CRC value based on 47-bit data — 410

Fix up encoded data based on invert bits — 412

Check whether error is from sequence # or transmission error — 414

DONE

FIG. 4C